

L Number	Hits	Search Text	DB	Time stamp
-	161	((master or active) adj controller) and phase and (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 14:31
-	2	((master or active) adj controller) same phase same (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 14:32
-	324	((master or active) adj controller) same phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 14:33
-	13	((master or active) adj controller) same (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 14:57
-	2	5870538.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 15:09
-	34	controller same signal same clock same (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 16:01
-	0	(master and slave) same controller same signal same clock same (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 16:01
-	90	(master and slave) same controller same signal same clock same phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 16:55
-	580	(master and slave) same controller same clock same signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 16:59
-	1	(master and slave) same controller same (stuck adj2 fault) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/06 17:02
-	43	controller same (stuck adj2 fault)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 08:59
-	213	controller and (stuck adj2 fault) and clock and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 08:19
-	22	controller same (stuck adj2 fault) and clock and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 08:36
-	137	714/\$.cc1s. and controller and (stuck adj2 fault) and clock and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 08:19

-	7	telica.as.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 08:36
-	31	(stuck adj2 fault) and signal and (out adj2 phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 10:14
-	133	714/11,12.ccls. and (transparent or failover or "fail over")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 10:19
-	18	714/11,12.ccls. and ((transparent or failover or "fail over") same signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 10:36
-	5	714/11,12.ccls. and ((transparent or failover or "fail over") same clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 10:40
-	61	714/11,12.ccls. and ((transparent or failover or "fail over") and (master or slave))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 10:43
-	28	714/11,12.ccls. and ((transparent or failover or "fail over") and (master or slave) and synchronous)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:06
-	3	714/11,12.ccls. and (time adj varying)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:10
-	31	714/\$.ccls. and (time adj varying adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:11
-	108	714/11,12.ccls. and (clock adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:13
-	0	714/11,12.ccls. and ((clock adj signal) same heartbeat)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:12
-	4	714/\$.ccls. and ((clock adj signal) same heartbeat)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:12
-	66	714/11,12.ccls. and (clock adj signal) and (master or master or active) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:15
-	74	714/11,12.ccls. and (clock\$3 adj3 signal) and ((master or master or active) and controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:15

-	3	714/11,12.ccls. and (clock\$3 adj3 signal) and ((master or master or active) adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:32
-	73	((clock\$3 adj3 signal) same heartbeat)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:33
-	27	((clock\$3 adj3 signal) same heartbeat) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 07:44
-	97	709/208,209,210,211.ccls. and (heartbeat or (clock\$3 adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 15:58
-	10	709/208,209,210,211.ccls. and (heartbeat or (clock\$3 adj signal)) and ((master or slave) adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 16:00
-	59	700/3.ccls. and (heartbeat or (clock\$3 adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 16:01
-	13	700/3.ccls. and (heartbeat or (clock\$3 adj signal)) and ((master or slave) adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 16:01
-	3	cisco.as. and ((clock\$3 adj3 signal) same heartbeat) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 11:16
-	3	cisco.as. and ((clock\$3 adj3 signal) same heartbeat) and controller and monitor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:33
-	6	(redundant adj signal) and (master adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:10
-	378	(redundant adj signal) and (controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:12
-	0	714/\$.ccls. and ((redundant adj signal) adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:44
-	54	714/\$.ccls. and ((redundant adj signal) and controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:48
-	0	714/\$.ccls. and ((redundant adj signal) same advantage same controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:49

-	1	((redundant adj signal) same (advantag\$3 or benefits) same controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:53
-	1	((redundant adj signal) same (reliabl\$3) same controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 13:57
-	25	(redundant adj signal) same (reliabl\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:01
-	78	(redundant adj signal) same (reliab\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:02
-	14	714/\$.ccls. and (redundant adj signal) same (reliab\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:21
-	17	714/\$.ccls. and ((redundant or duplicate) adj signal) same (reliab\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:10
-	3	714/\$.ccls. and ((redundant or duplicate) adj signal) same (reliab\$5) not (714/\$.ccls. and (redundant adj signal) same (reliab\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:10
-	0	redundant adj trace adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:12
-	151	714/\$.ccls. and (redundant adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:13
-	2	714/\$.ccls. and (redundant adj signal) adj provid\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:14
-	3	(redundant adj signal) adj provid\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:14
-	0	3*920914.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 14:21
-	2	3920914.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 08:24
-	15	(redundant adj signals) and (controller adj3 (master or primary))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 08:35

-	1	(send\$3 adj ((redundant or duplicate) adj signals)) and (controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 08:36
-	4	(send\$3 adj ((redundant or duplicate) adj signals))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 08:39
-	16	714/\$.ccls. and (copy adj2 signals) same controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:34
-	8	emerson.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:10
-	169	714/\$.ccls. and controller and (master or primary or active) and (out adj2 (phase or synch))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:45
-	163	714/\$.ccls. and controller and (master or primary or active) and (out adj2 (phase or synch)) and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:54
-	87	714/\$.ccls. and (controller same (master or primary or active)) and (out adj2 (phase or synch)) and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:55
-	63	714/\$.ccls. and (controller with (master or primary or active)) and (out adj2 (phase or synch)) and signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:58
-	65	714/\$.ccls. and (controller with (master or primary or active)) and (out adj2 (phase or synch))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 09:59
-	12	714/11,12.ccls. and (controller with (master or primary or active)) and (out adj2 (phase or synch))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 10:15
-	23	714/11,12.ccls. and ((controller or CPU or processor) with (master or primary or active)) and (out adj2 (phase or synch))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 10:16
-	12	714/11,12.ccls. and ((controller or CPU or processor) adj2 ((master or primary or active) or (backup or secondary or slave or passive))) same (((out or "in") adj2 (phase or synch\$5)) or (lock adj step))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:18
-	66	((controller or CPU or processor) adj2 ((master or primary or active) or (backup or secondary or slave or passive))) same (((out or "in") adj2 (phase or synch\$5)) or (lock adj step))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 10:49
-	7	((controller) adj2 ((master or primary or active) or (backup or secondary or slave or passive))) same (lock adj step)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:21

-	303	((master or primary or active) or (backup or secondary or slave or passive)) same (lock adj step)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:21
-	83	714/\$.ccls. and (((master or primary or active) or (backup or secondary or slave or passive)) same (lock adj step))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:35
-	1	714/\$.ccls. and heartbeat same lockstep	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:35
-	0	714/\$.ccls. and heartbeat same (lock adj step)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:36
-	1	heartbeat same (lock adj step)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:36
-	3	heartbeat same ((lock adj step) or lockstep)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:37
-	34	714/12.ccls. and ((lock adj step) or lockstep)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 13:30
-	10	714/12.ccls. and ((synchronous or synch or synchronize) same master) and slave	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 14:23